



Replacement Sheet

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100

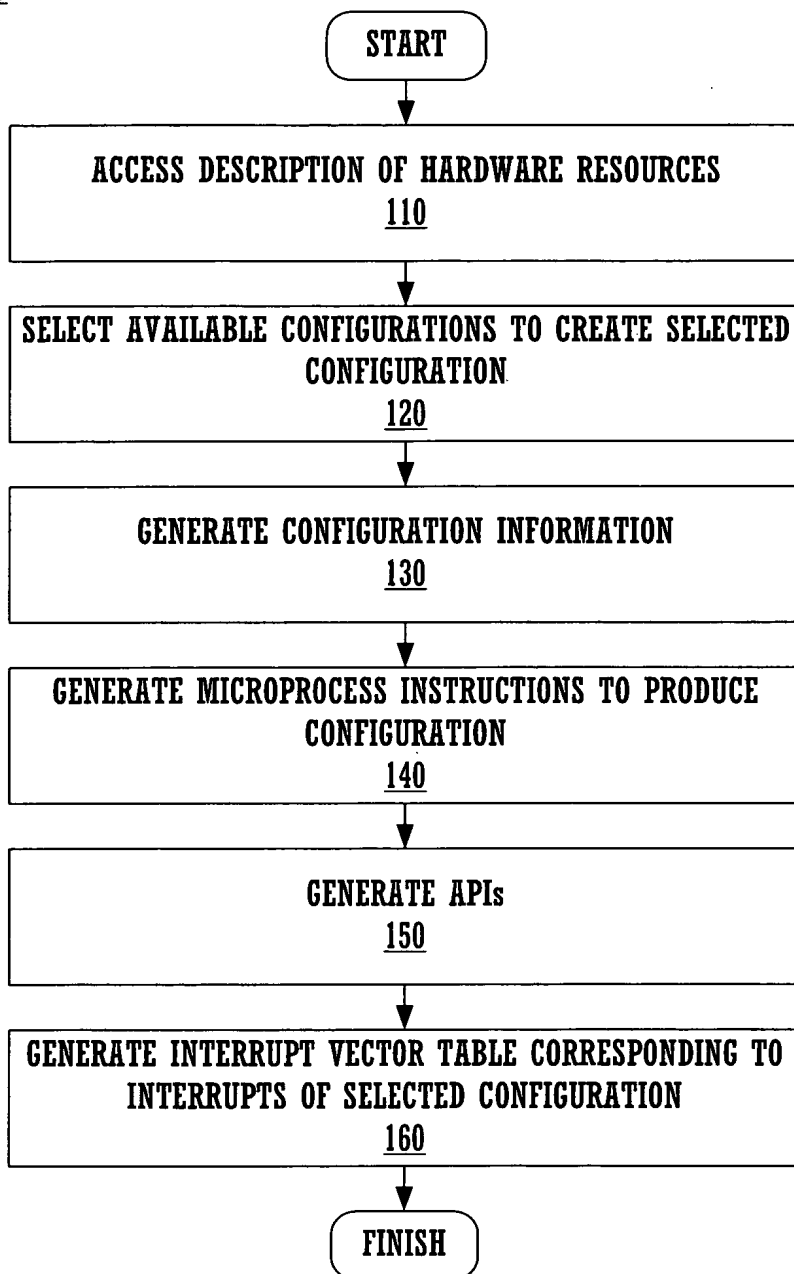
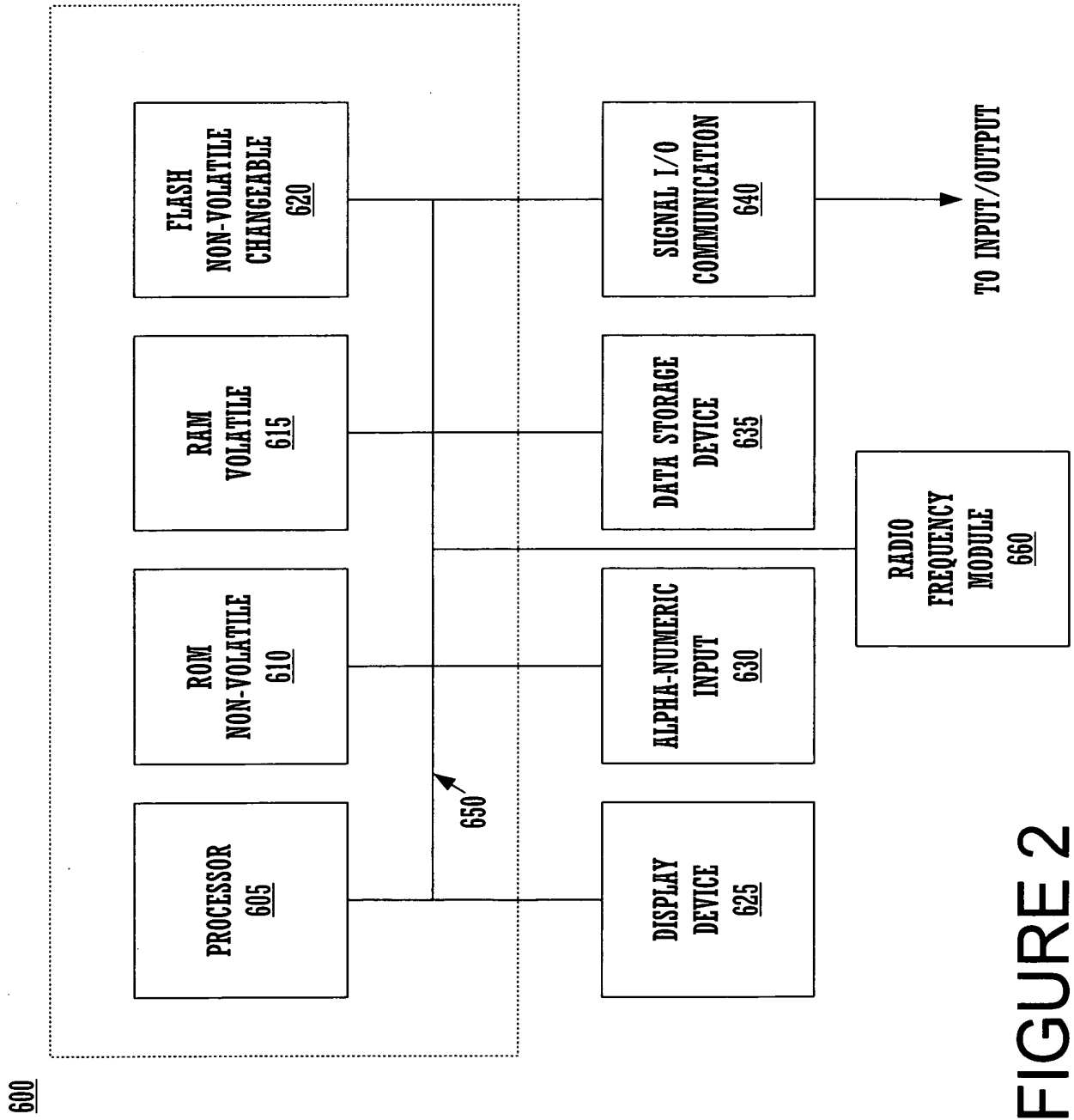


FIGURE 1

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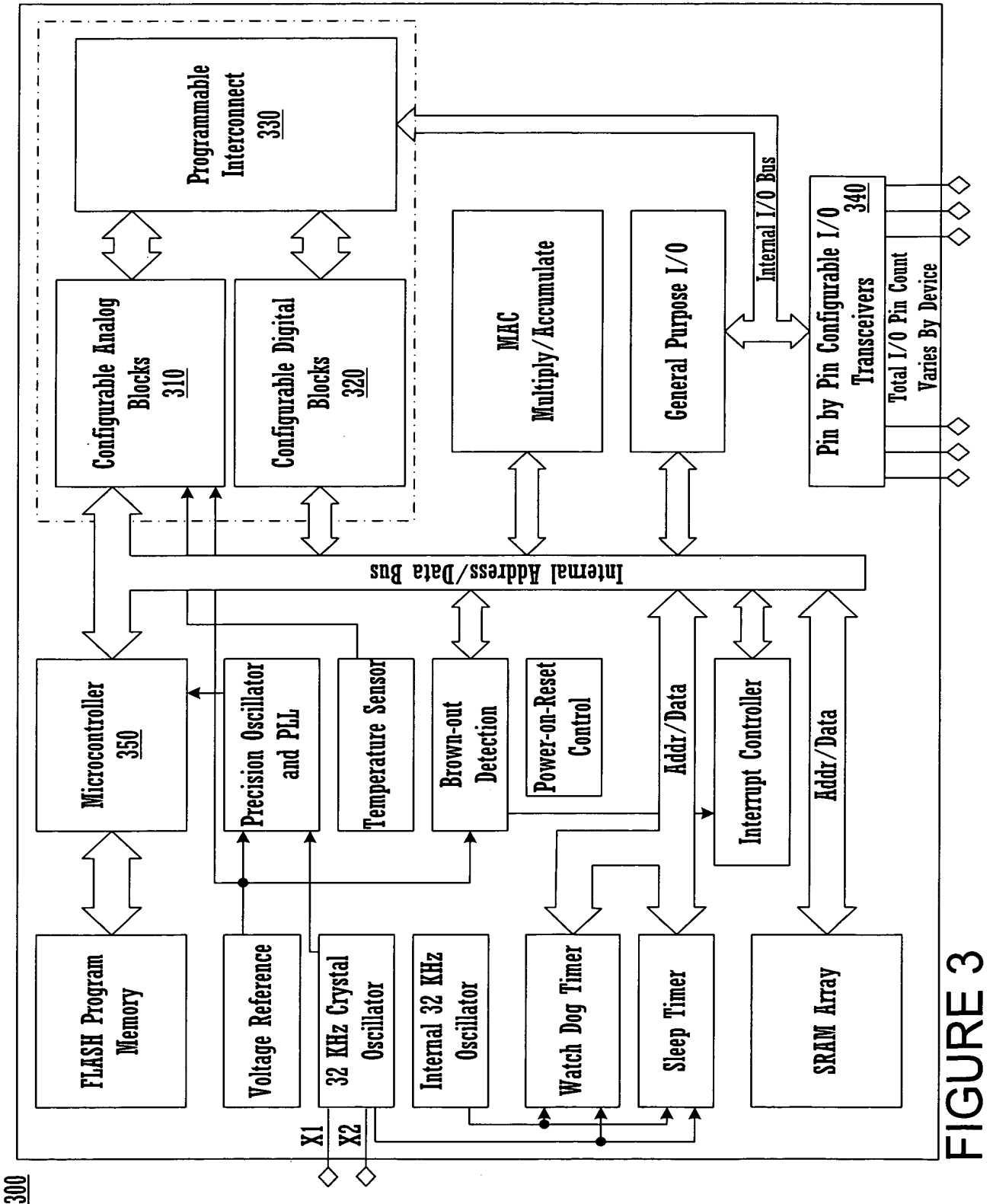
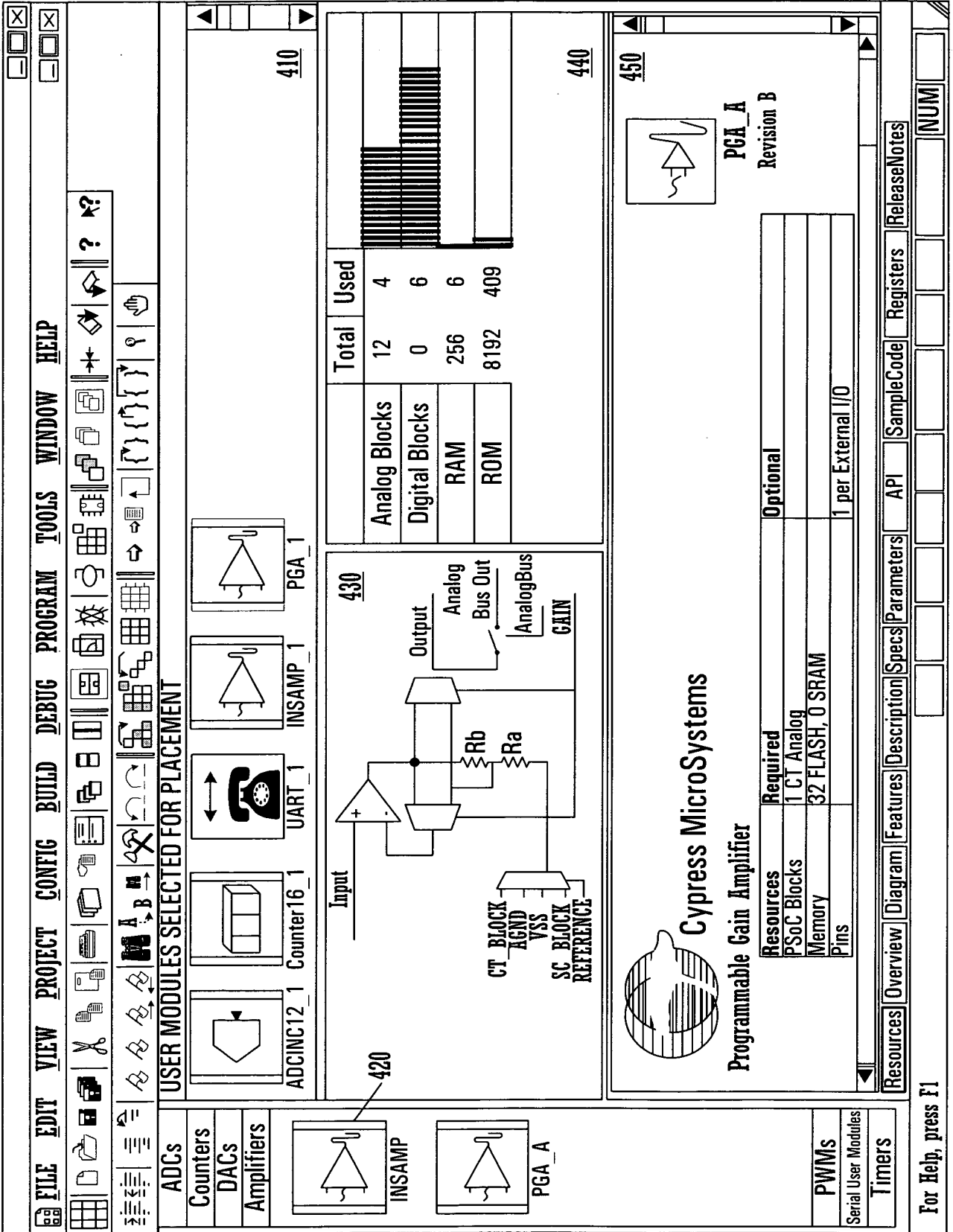


FIGURE 3

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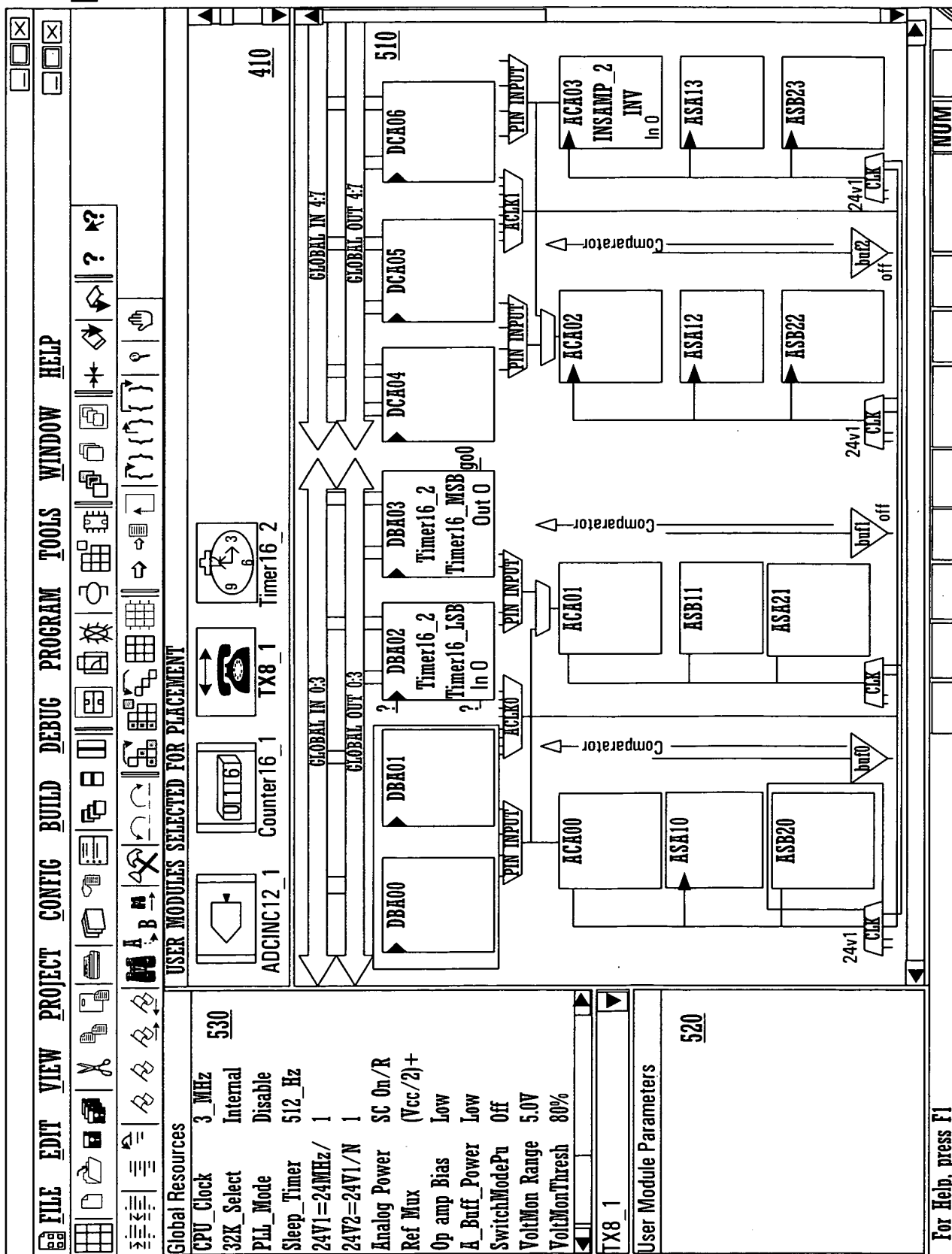
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400  
 FIGURE  
 4



For Help, press F1

**FIGURE 5**





```

700
702 <!--DEVICE_DB>
704 <!--USER_MODULE_LIST>
706 <!--Variable Resolution Incremental ADC-->
707 <USER_MODULE_NAME="ADCEXP" TYPE="PSOC_ADC" HTML="ADCEXP.HTM" ICON="ADC.ico" METAFILE="ADCEXP.emf"
API_PATH_TYPE="CUSTOM">
708 <SHAPE_SHAPE_TYPE="BLOCKLIST">
710 <BLOCK_LIST>
712 <BLOCK NAME="ADC" TYPE="ANALOG_SC">
714 <REGISTER_LIST>
716 <REGISTER NAME="CRO">
718 <BITFIELD_LIST>
720 <BITFIELD NAME="OSZ" VALUE="OSZ_32"/>
722 <BITFIELD NAME="CLOCK_PHASE" VALUE="PHASE_NORM"/>
724 <BITFIELD NAME="SIGN" VALUE="POS"/>
726 <BITFIELD NAME="CAP_SIZE" VALUE="16"/>
728 </BITFIELD_LIST>
730 </REGISTER>
732 <REGISTER NAME="CRI">
734 <BITFIELD_LIST>
736 <BITFIELD NAME="CAP_SIZE" VALUE="0"/>
738 </BITFIELD_LIST>
740 </REGISTER>
742 <REGISTER NAME="CR2">
744 <BITFIELD_LIST>
746 <BITFIELD NAME="A_OUT" VALUE="DISABLED"/>
748 <BITFIELD NAME="COMP_OUT" VALUE="ENABLED"/>
750 <BITFIELD NAME="AZ_PHASE" VALUE="AZ_PHI"/>

```

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```
<BITFIELD NAME="CAP_SIZE" VALUE="0"/>
</BITFIELD_LIST>
</REGISTER>
<REGISTER NAME="CR3">
<BITFIELD_LIST>
  <BITFIELD NAME="REF_SELECT" VALUE="COMP"/>
  <BITFIELD NAME="FB_CAP_SELECT" VALUE="PH2_AZ_ENABLE"/>
  <BITFIELD NAME="FB_CAP_GND" VALUE="GND_PHI"/>
  <BITFIELD NAME="SCA_INPUT_MUX_B" VALUE="IN2B"/>
  <BITFIELD NAME="SCB_INPUT_MUX_B" VALUE="IN2B"/>
  <BITFIELD NAME="SBB" VALUE="DISABLE"/>
  <BITFIELD NAME="PWR_SELECT" VALUE="PWR_OFF"/>
</BITFIELD_LIST>
</REGISTER>
</REGISTER_LIST>
720 — <INPUT_LIST/>
      </BLOCK>
      </BLOCK_LIST>
      <BLOCK_LIST>
        <BLOCK NAME="TMR" TYPE="DIGITAL" INTERRUPT_SOURCE="TMR_INT" INTERRUPT_TYPE="JUMP">
          <REGISTER_LIST>
```

FIGURE 7B



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```
<REGISTER NAME="DIG_BasicFunction">
<BITFIELD_LIST>
  <BITFIELD NAME="END" VALUE="IsEnd"/>
  <BITFIELD NAME="CompareType" VALUE="LTorEQ"/>
  <BITFIELD NAME="InterruptType" VALUE="TerminalCount"/>
  <BITFIELD NAME="Function" VALUE="Timer"/>
</BITFIELD_LIST>
</REGISTER>
<REGISTER NAME="DIG_Input">
<BITFIELD_LIST>
  <BITFIELD NAME="DataSelect" VALUE="High"/>
</BITFIELD_LIST>
</REGISTER>
```

FIGURE 7B (Continued)

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```
<REGISTER NAME="DIG_Output">
  <BITFIELD_LIST>
    <BITFIELD NAME="AUXOutputEnable" VALUE="DISABLE"/>
    <BITFIELD NAME="DigOutputEnable" VALUE="DISABLE"/>
  </BITFIELD_LIST>
</REGISTER>
</REGISTER_LIST>
<INPUT_LIST/>
</BLOCK>
</BLOCK_LIST>
<BLOCK_LIST>
  <BLOCK NAME="CNT_LSB" TYPE="DIGITAL">
    <REGISTER_LIST>
      <REGISTER NAME="DIG_BasicFunction">
        <BITFIELD_LIST>
          <BITFIELD NAME="Function" VALUE="Counter"/>
          <BITFIELD NAME="CompareType" VALUE="LTorEQ"/>
        </BITFIELD_LIST>
      </REGISTER>
    </REGISTER_LIST>
  </BLOCK>
</BLOCK_LIST>
```

FIGURE 7C

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```
<INPUT_LIST>
  <INPUT SOURCE="AnalogOut" TYPE="RESERVED_RESOURCE" REGISTER_NAME="DIG_Input" BITFIELD="DataSelect"/>
</INPUT_LIST>
</BLOCK>
<BLOCK NAME="CNT_MSB" TYPE="DIGITAL" INTERRUPT_SOURCE="_CNT_INT" INTERRUPT_TYPE="JUMP">
  <REGISTER_LIST>
    <REGISTER NAME="DIG_BasicFunction">
      <BITFIELD_LIST>
        <BITFIELD NAME = "Function" VALUE="Counter"/>
        <BITFIELD NAME = "END" VALUE="IsEnd"/>
        <BITFIELD NAME = "CompareType" VALUE="LTorEQ"/>
      </BITFIELD_LIST>
    </REGISTER>
  </REGISTER_LIST>
</BLOCK>
```

FIGURE 7C (Continued)

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```

<REGISTER_LIST>
<INPUT_LIST>
  <INPUT SOURCE="CNT_LSB" TYPE="BLOCK" REGISTER_NAME="DIG_Input" BITFIELD="DataSelect"/>
</INPUT_LIST>
</BLOCK>
</BLOCK_LIST>
<RESOURCE_LIST>
  <RESOURCE NAME="AnalogOut" TYPE="ANALOG_COMPARATOR_OUTPUT">
725 —
    <INPUT_LIST>
730 —
      <INPUT SOURCE="ADC" TYPE="BLOCK"/>
    </INPUT_LIST>
  </RESOURCE>
</RESOURCE_LIST>
</SHAPE>
740 — <PARAMETER_LIST>
  <PARAMETER NAME="ADCINPUT_SCB" TYPE="INPUT" SOURCE="ADC" REGISTER_NAME="CRI"
    BITFIELD="SCB_INPUT_MUX_A"/>
  <PARAMETER NAME="ADCINPUT_SCA" TYPE="INPUT" SOURCE="ADC" REGISTER_NAME="CRI"
    BITFIELD="SCA_INPUT_MUX_A_C"/>
  <PARAMETER NAME="ClockPhase" TYPE="BLOCK" SOURCE="ADC" REGISTER_NAME="CRO"
    BITFIELD="CLOCK_PHASE"/>
  <PARAMETER NAME="TMRCLK" TYPE="CLOCK" SOURCE="TMR" REGISTER_NAME="DIG_Input"
    BITFIELD="ClockSelect"/>
  <PARAMETER NAME="CTRLCLK" TYPE="CLOCK" SOURCE="CNT_LSB" REGISTER_NAME="DIG_Input"
    BITFIELD="ClockSelect"/>
  <PARAMETER NAME="CTRLCLKM" TYPE="CLOCK" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Input"
    BITFIELD="ClockSelect"/>

```

FIGURE 7D

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```
742 </PARAMETER_LIST>  
    <API_REGISTER_ALIAS_LIST>  
744 <API_REGISTER_ALIAS_NAME="HtoDct0" SOURCE="ADC" REGISTER_NAME="CR0"/>  
    <API_REGISTER_ALIAS_NAME="HtoDct1" SOURCE="ADC" REGISTER_NAME="CR1"/>  
    <API_REGISTER_ALIAS_NAME="HtoDct2" SOURCE="ADC" REGISTER_NAME="CR2"/>  
    <API_REGISTER_ALIAS_NAME="HtoDct3" SOURCE="ADC" REGISTER_NAME="CR3"/>  
    <API_REGISTER_ALIAS_NAME="CounterMSBFN" SOURCE="CNT_MSB" REGISTER_NAME="DIG_BasicFunction"/>
```

FIGURE 7D (Continued)

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```
<API_REGISTER_ALIAS_NAME="CounterMSBSL" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Input"/>
<API_REGISTER_ALIAS_NAME="CounterMSBOS" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Output"/>
<API_REGISTER_ALIAS_NAME="CounterMSBDR0" SOURCE="CNT_MSB" REGISTER_NAME="DATA_0"/>
<API_REGISTER_ALIAS_NAME="CounterMSBDR1" SOURCE="CNT_MSB" REGISTER_NAME="DATA_1"/>
<API_REGISTER_ALIAS_NAME="CounterMSBDR2" SOURCE="CNT_MSB" REGISTER_NAME="DATA_2"/>
<API_REGISTER_ALIAS_NAME="CounterMSBCR0" SOURCE="CNT_MSB" REGISTER_NAME="CONTROL_0"/>
<API_REGISTER_ALIAS_NAME="CounterLSBFN" SOURCE="CNT_LSB" REGISTER_NAME="DIG_BasicFunction"/>
<API_REGISTER_ALIAS_NAME="CounterLSBSL" SOURCE="CNT_LSB" REGISTER_NAME="DIG_Input"/>
<API_REGISTER_ALIAS_NAME="CounterLSBOS" SOURCE="CNT_LSB" REGISTER_NAME="DIG_Output"/>
<API_REGISTER_ALIAS_NAME="CounterLSBDR0" SOURCE="CNT_LSB" REGISTER_NAME="DATA_0"/>
<API_REGISTER_ALIAS_NAME="CounterLSBDR1" SOURCE="CNT_LSB" REGISTER_NAME="DATA_1"/>
<API_REGISTER_ALIAS_NAME="CounterLSBDR2" SOURCE="CNT_LSB" REGISTER_NAME="DATA_2"/>
<API_REGISTER_ALIAS_NAME="CounterLSBCR0" SOURCE="CNT_LSB" REGISTER_NAME="CONTROL_0"/>
<API_REGISTER_ALIAS_NAME="TimerFN" SOURCE="TMR" REGISTER_NAME="Dig_Input"/>
<API_REGISTER_ALIAS_NAME="TimerSL" SOURCE="TMR" REGISTER_NAME="Dig_Output"/>
<API_REGISTER_ALIAS_NAME="TimerOS" SOURCE="TMR" REGISTER_NAME="Data_0"/>
<API_REGISTER_ALIAS_NAME="TimerDR0" SOURCE="TMR" REGISTER_NAME="Data_1"/>
<API_REGISTER_ALIAS_NAME="TimerDR1" SOURCE="TMR" REGISTER_NAME="Data_1"/>
<API_REGISTER_ALIAS_NAME="TimerDR2" SOURCE="TMR" REGISTER_NAME="Data_2"/>
<API_REGISTER_ALIAS_NAME="TimerCR0" SOURCE="TMR" REGISTER_NAME="CONTROL_0"/>
<API_REGISTER_ALIAS_NAME="Comp_Ctrl" SOURCE="AnalogComp" TYPE="RESERVED_RESOURCE"
REGISTER_NAME="AnalogComparatorControl"/>
</API_REGISTER_ALIAS_LIST>
</USER_MODULE>
</USER_MODULE_LIST>
</DEVICE_DB>
```

FIGURE 7E